

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Nisar A. Chaudhry

Group Art Unit: 2836

Serial No.: 10/635,341

Examiner: Boris Benenson

Filed: August 6, 2003

For: WIRING ERROR DETECTION CIRCUIT

**COMMUNICATION TO THE EXAMINER**

**SUBMITTING REPLACEMENT SHEET FOR FIG. 11**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Further to the fax on September 13, 2006 to Examiner Benenson of a proposed corrected drawing (Fig. 11) and in reply to the Notice of Allowability of August 8, 2006, enclosed herewith is a formal replacement sheet for Fig. 11. In the Notice, the Examiner has requested correction of the drawings to show for example how the wiring error detection circuit is connected to the various conductors. Accordingly, the Applicant has revised the drawing of Fig. 11 by adding dotted lines reflecting the different wires on element 1000 and the interconnection between the plug and the outlet, as is supported by the specification and as would be understood by one of ordinary skill in the art in view of the specification. No new matter has been added by this change which is believed to address the Examiner's concerns. Approval and entry of this drawing are respectfully requested.

No fees are believed to be due. However, the Commissioner is authorized to charge any additional fees which may be required for consideration of this paper to Deposit Account No. 13-4500, Order No. 0720-4147.

Respectfully submitted,  
MORGAN & FINNEGAN, L.L.P.

Dated: 10/12/06

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